

Fig. 2A
(PRIOR ART)

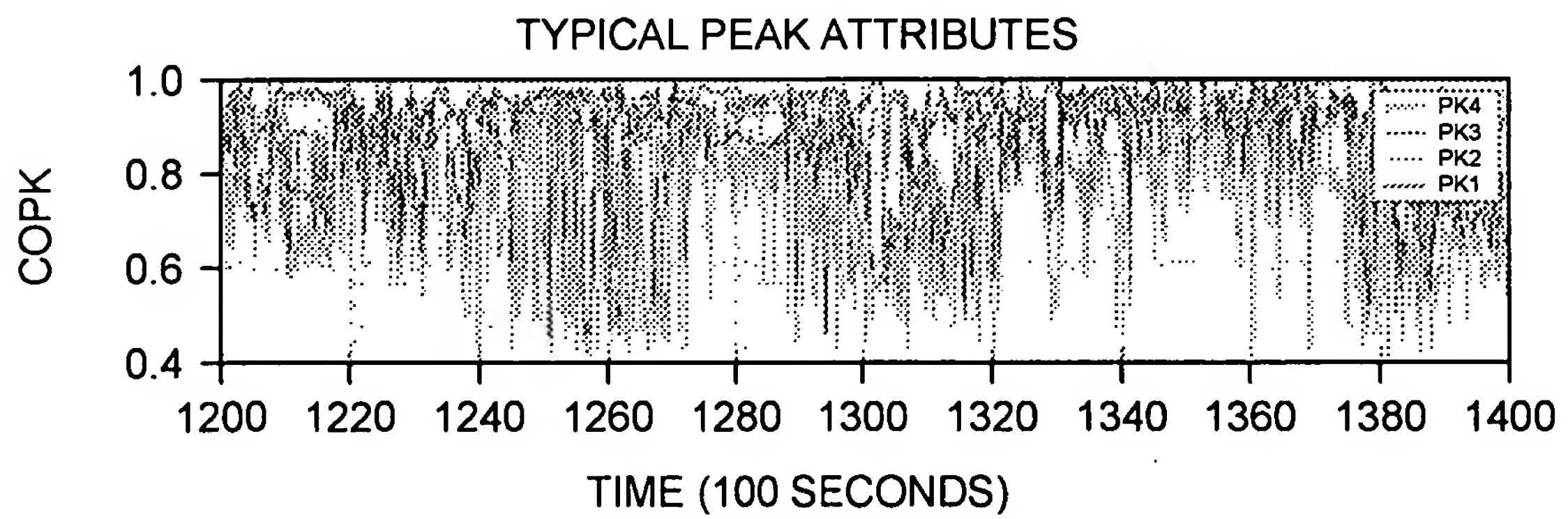


Fig. 2B
(PRIOR ART)

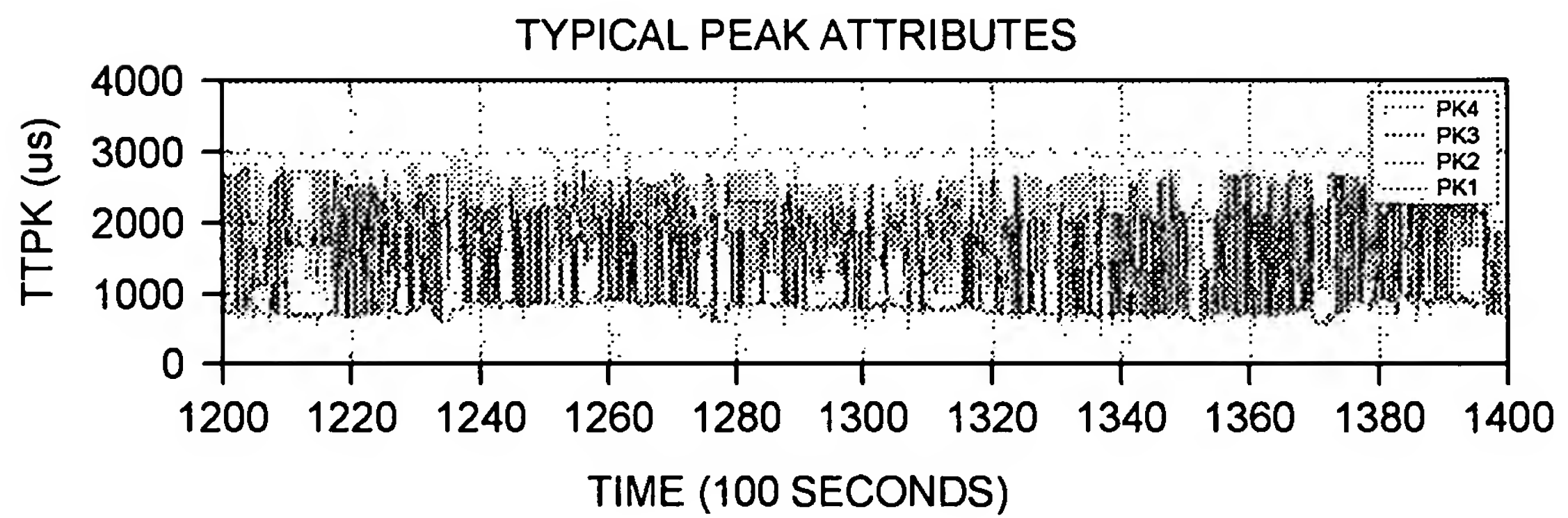


Fig. 2C
(PRIOR ART)

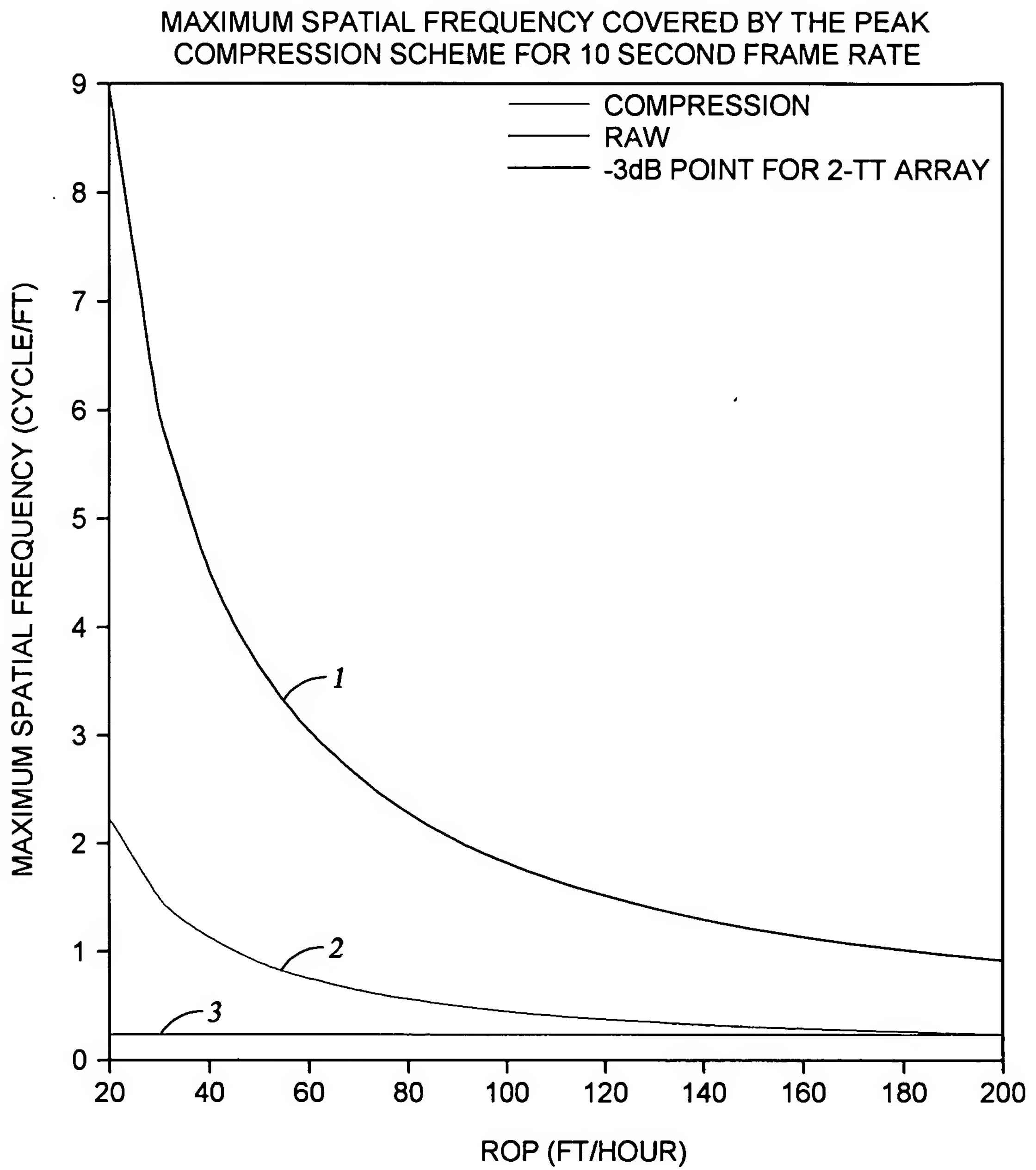
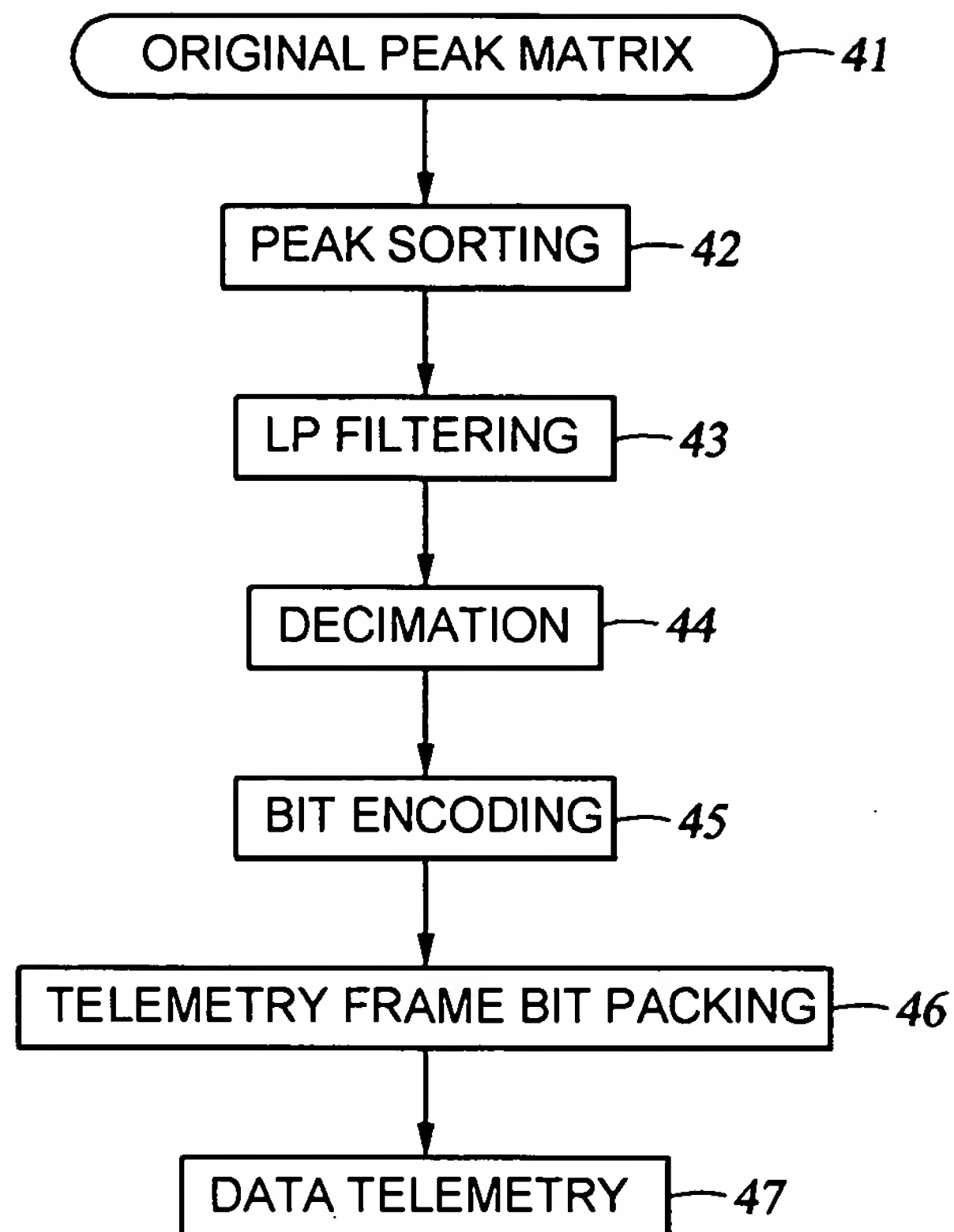
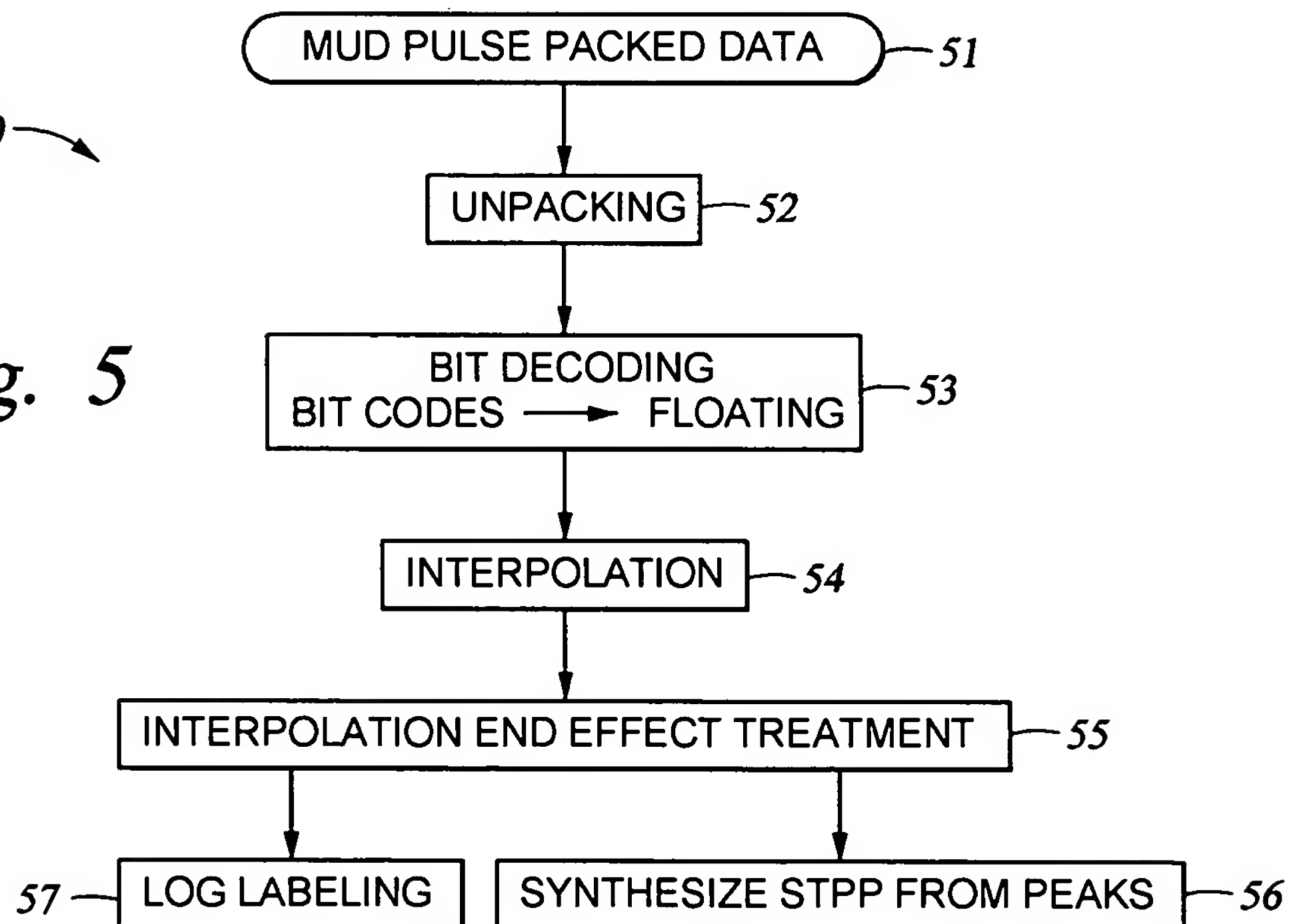
*Fig. 3*

Fig. 4

40 →

*Fig. 5*

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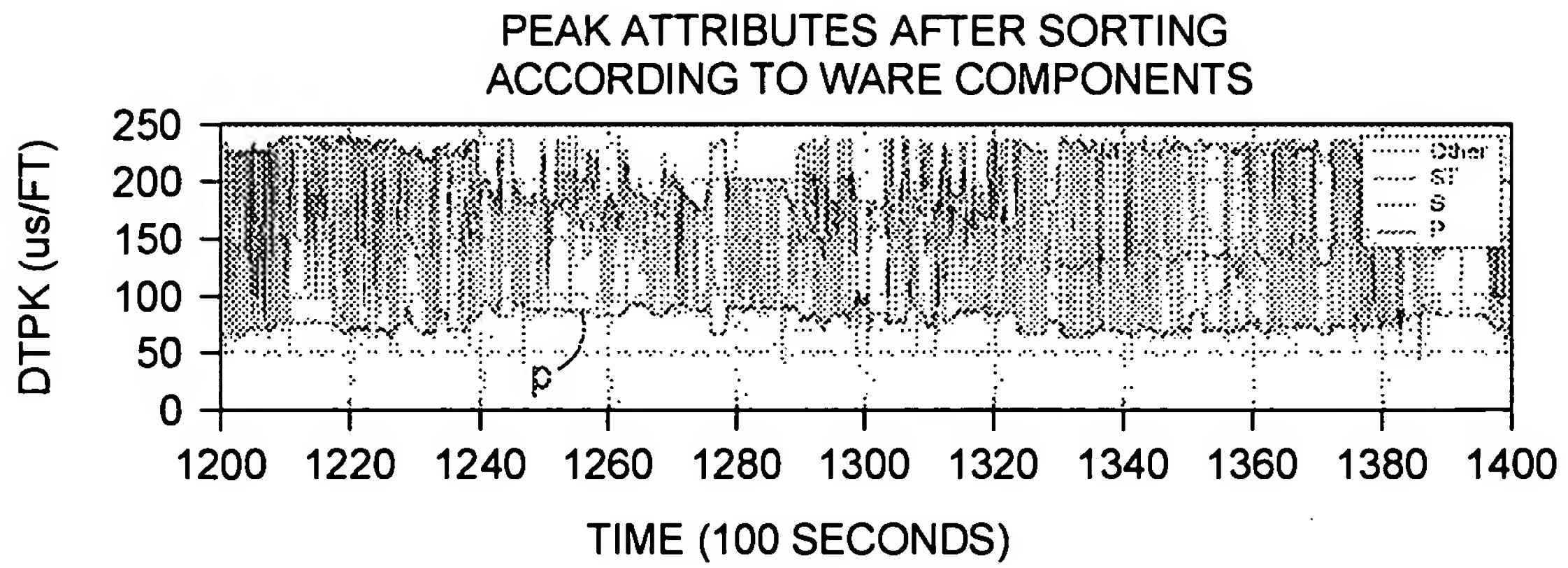


Fig. 6A

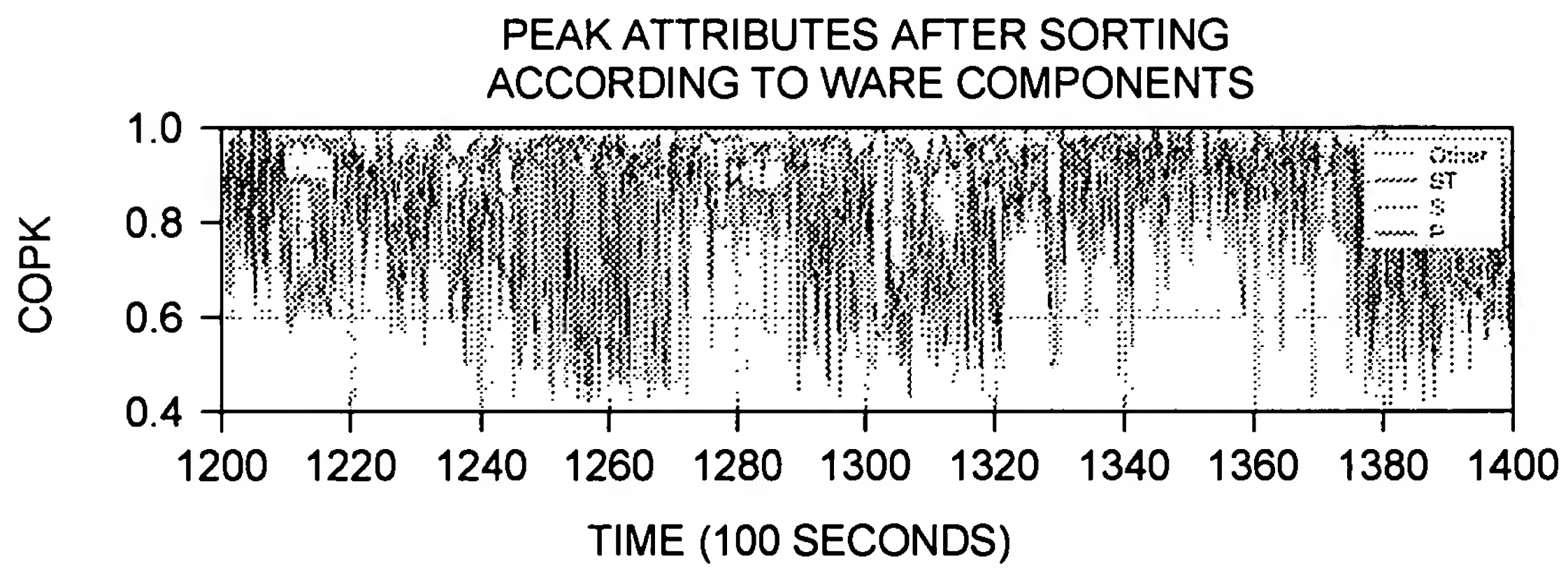


Fig. 6B

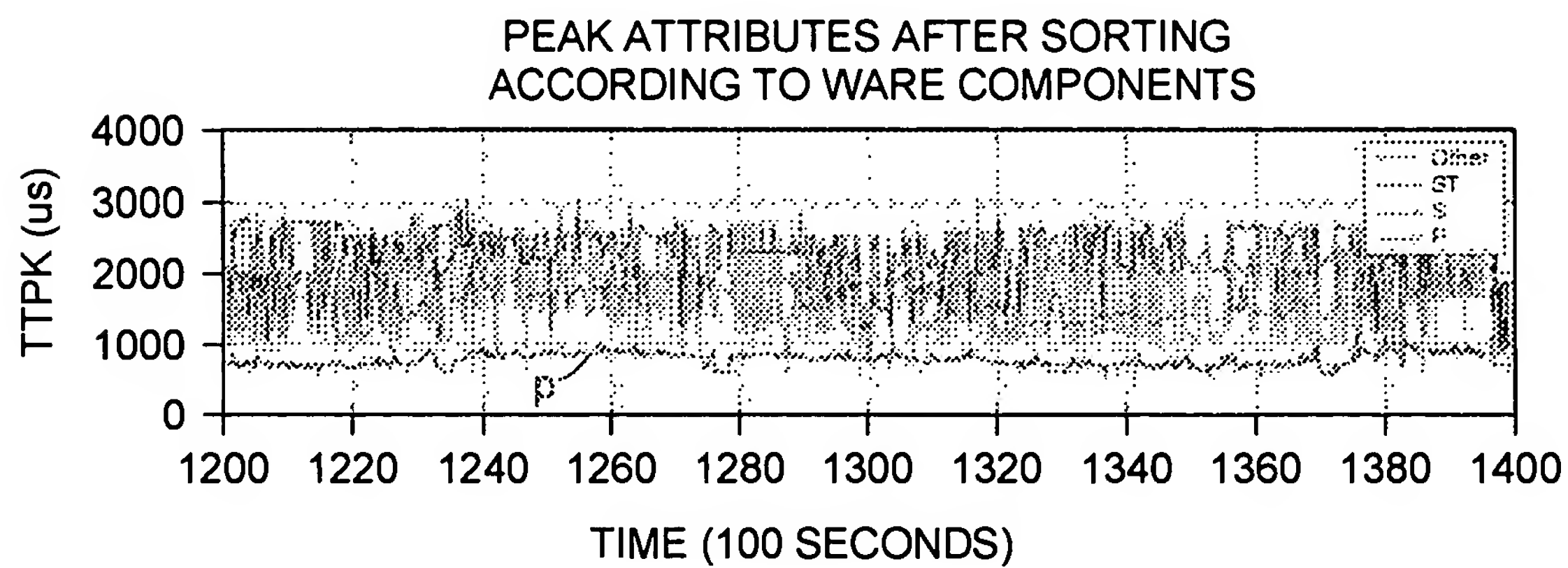
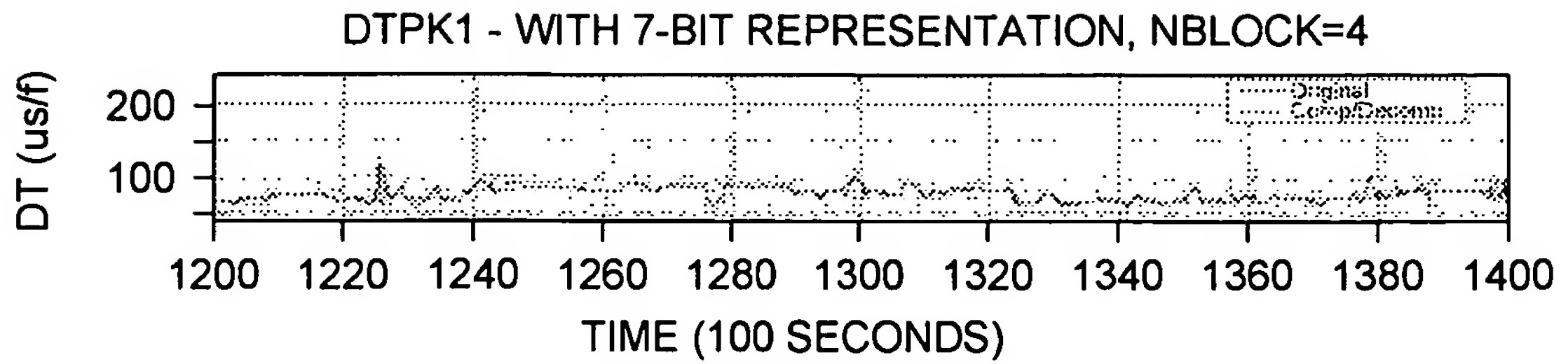
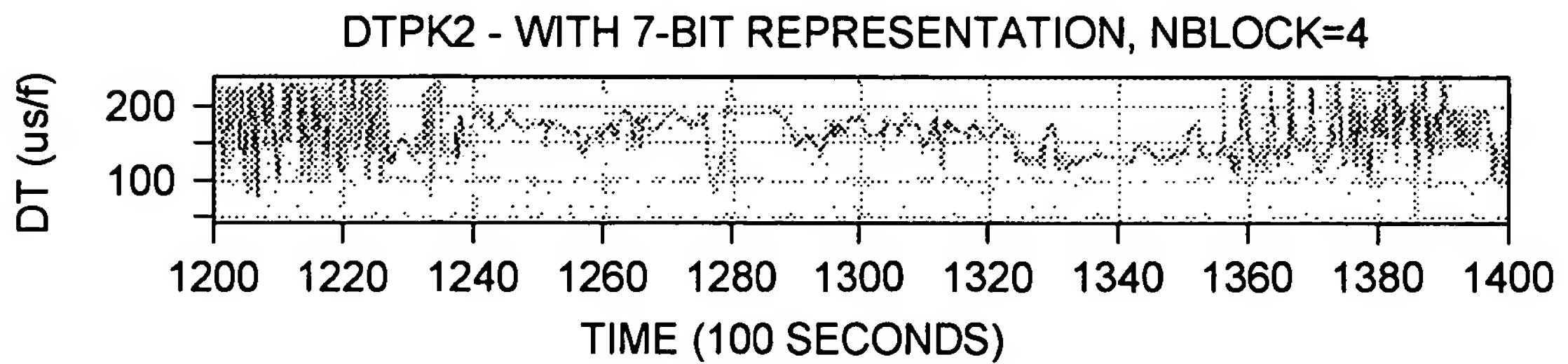
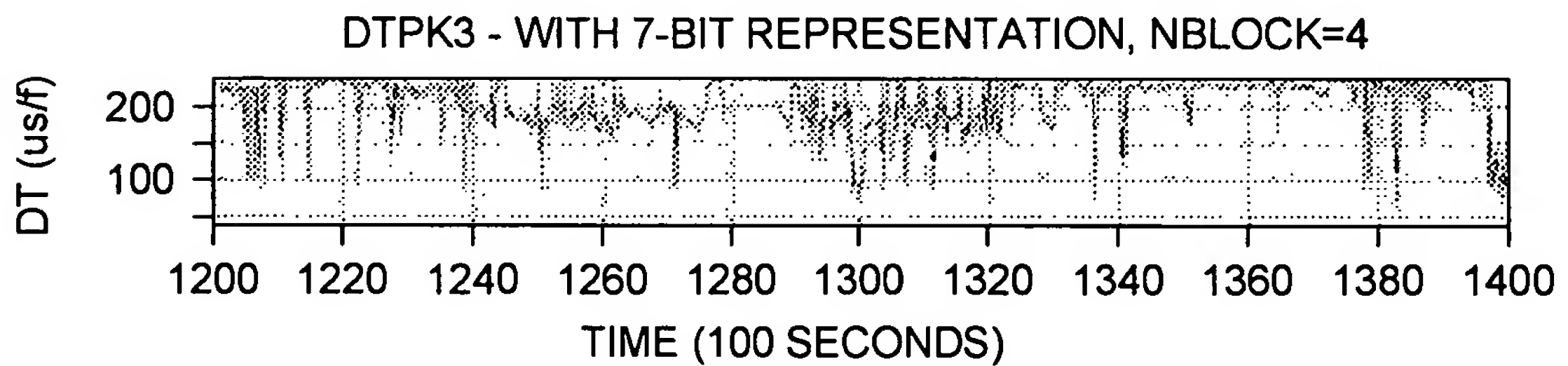
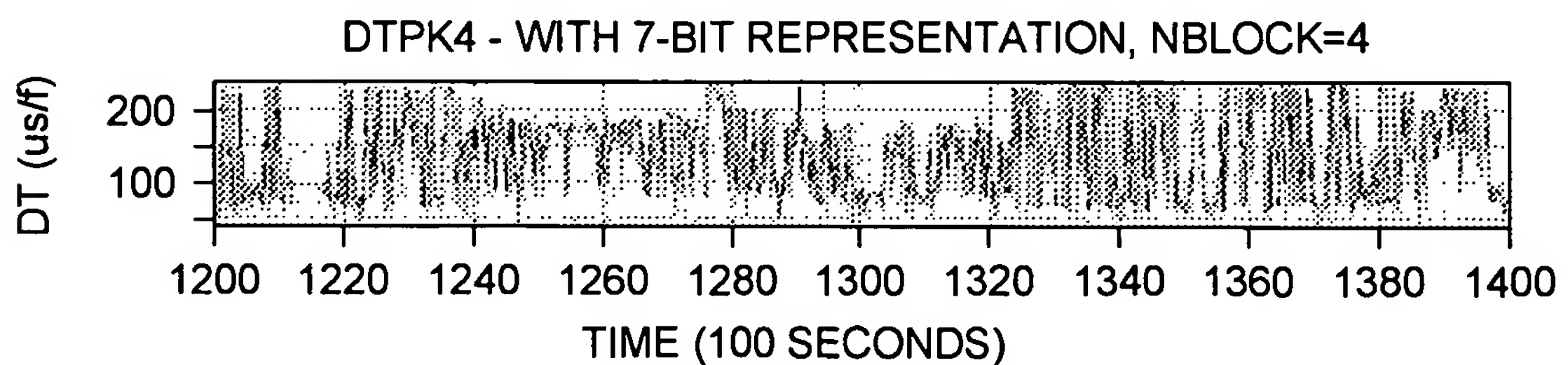
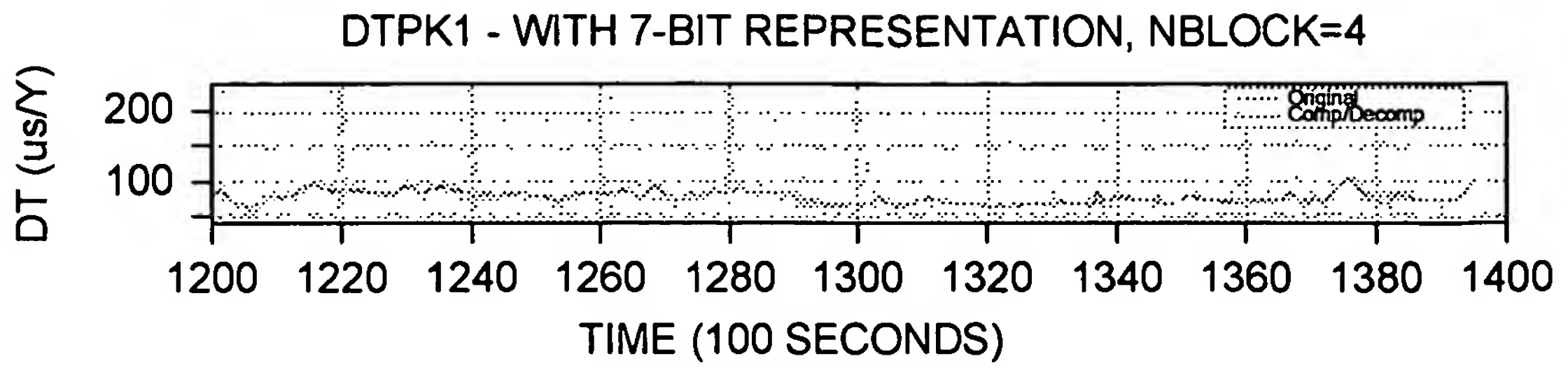
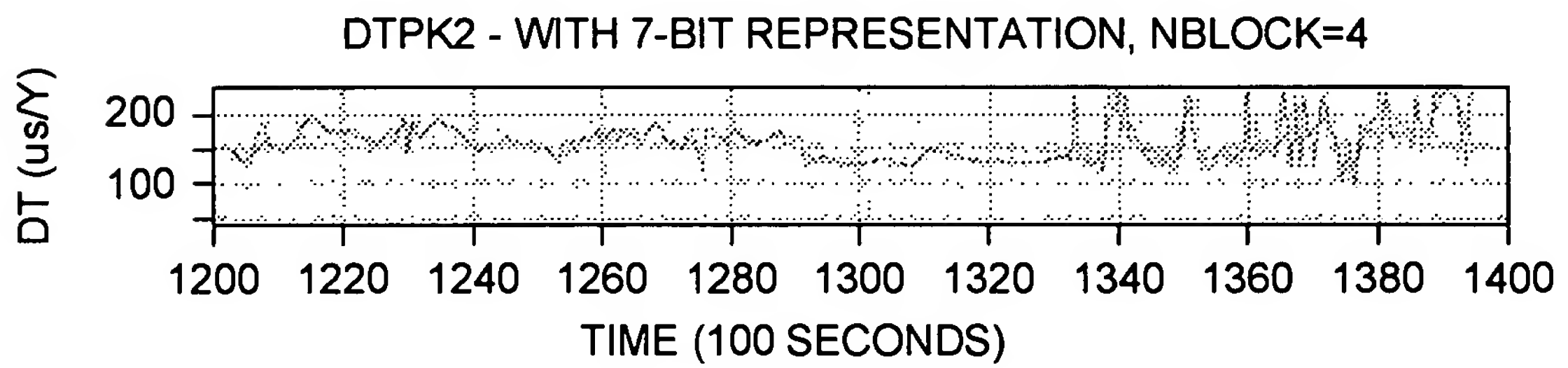
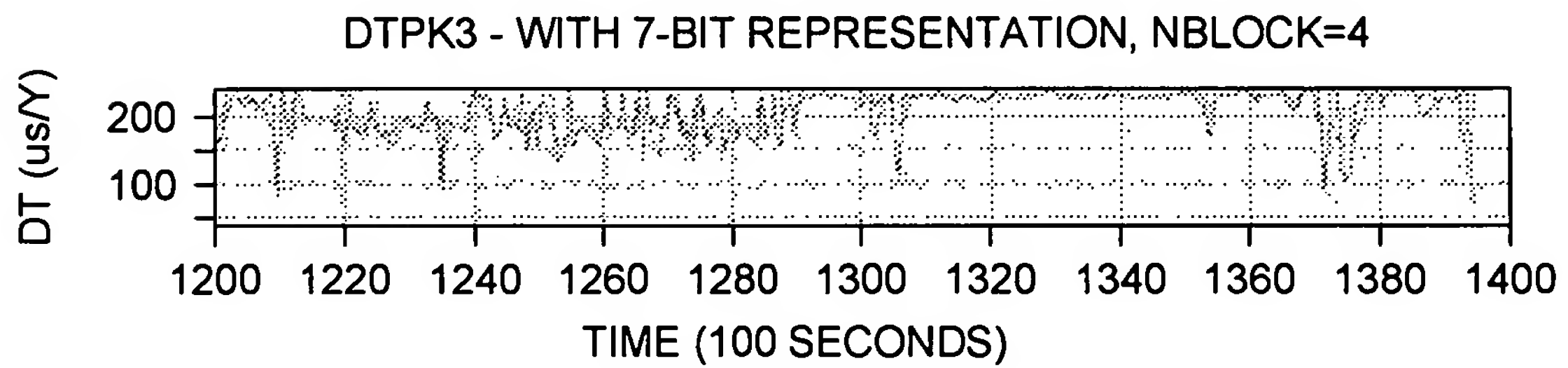
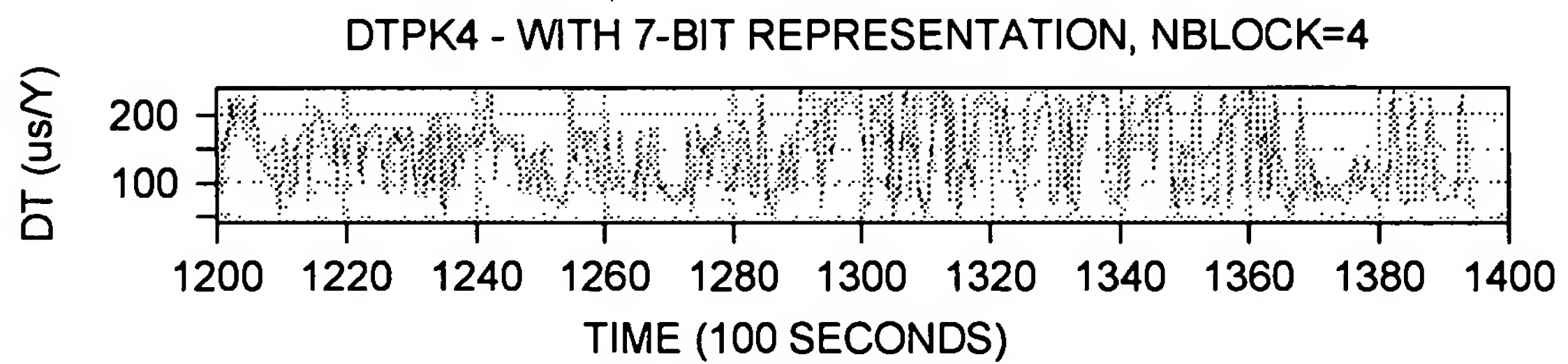
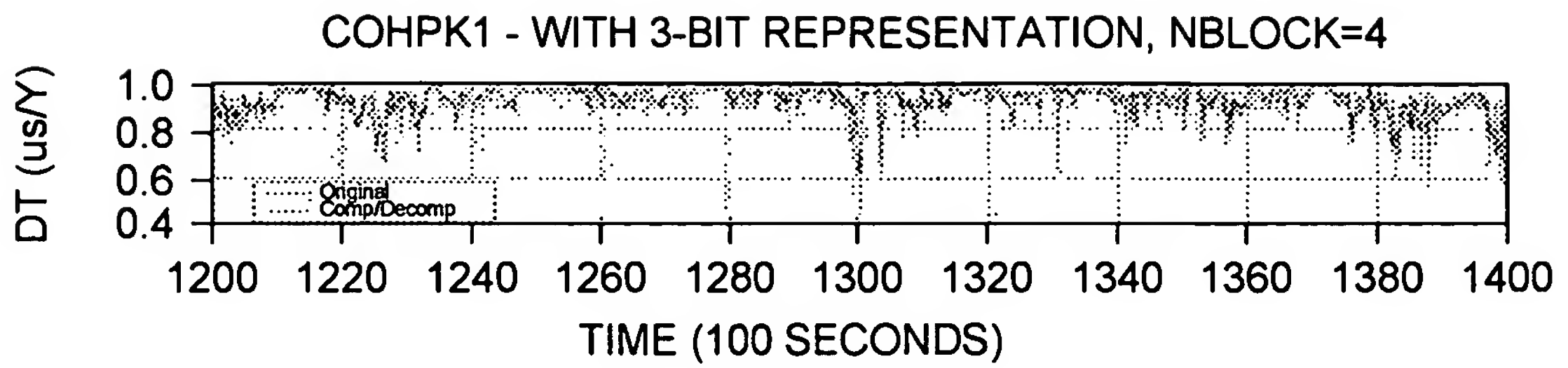
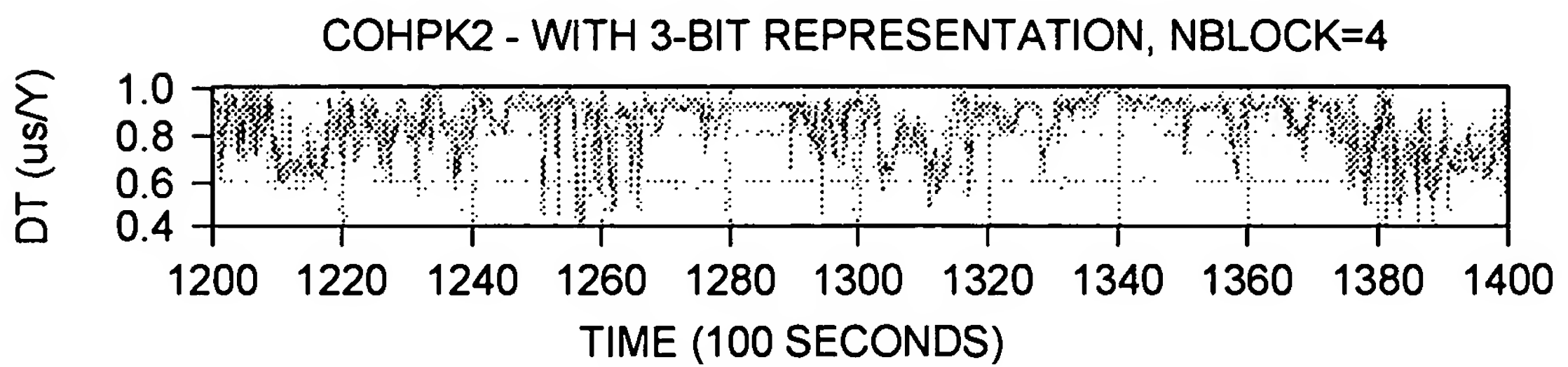
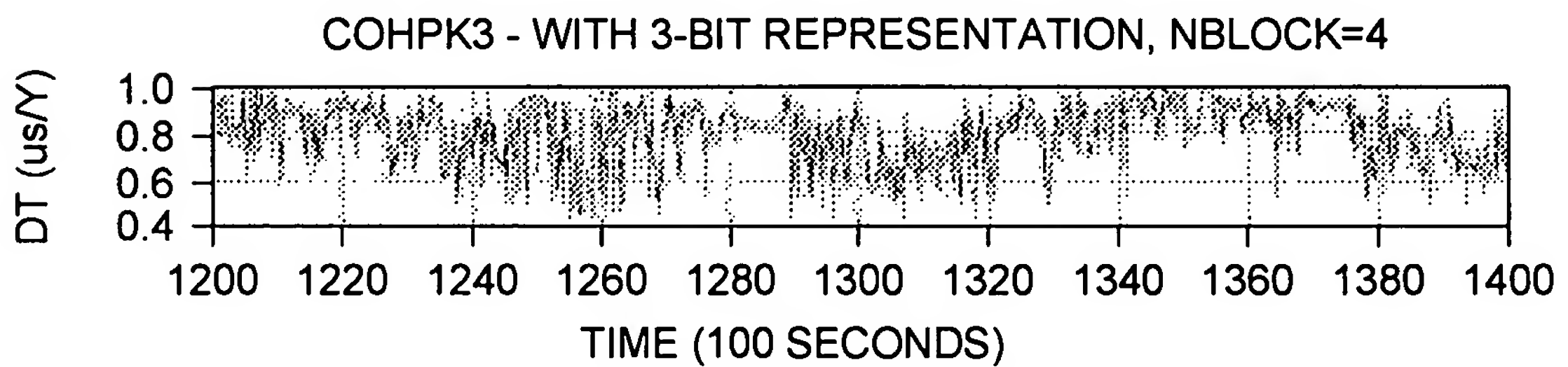
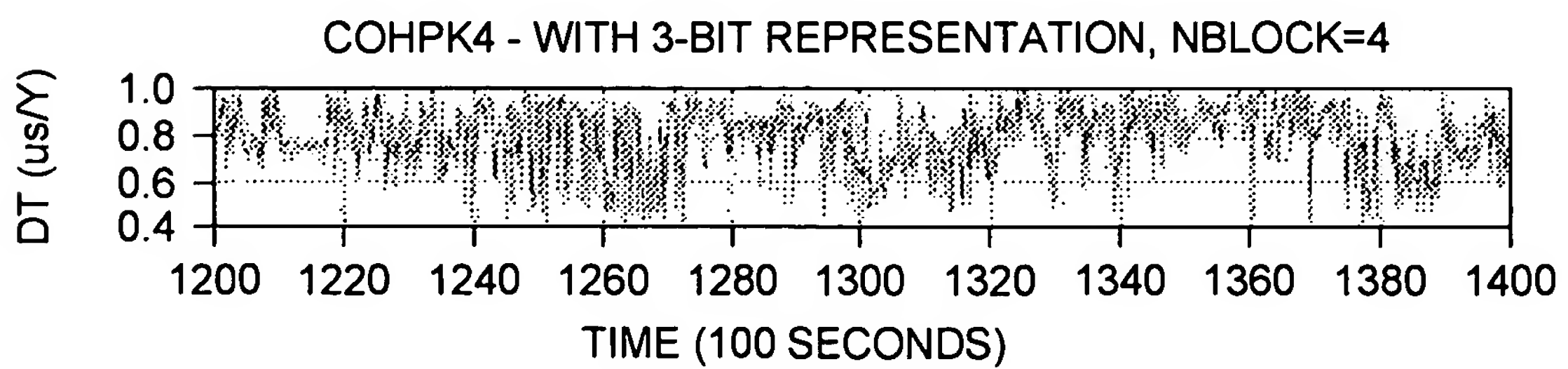
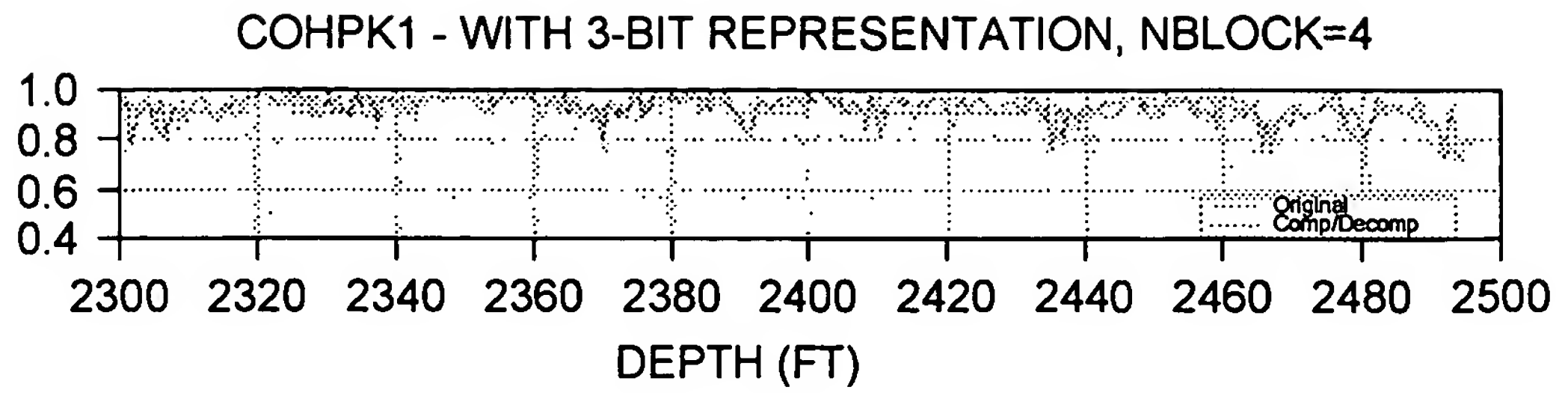
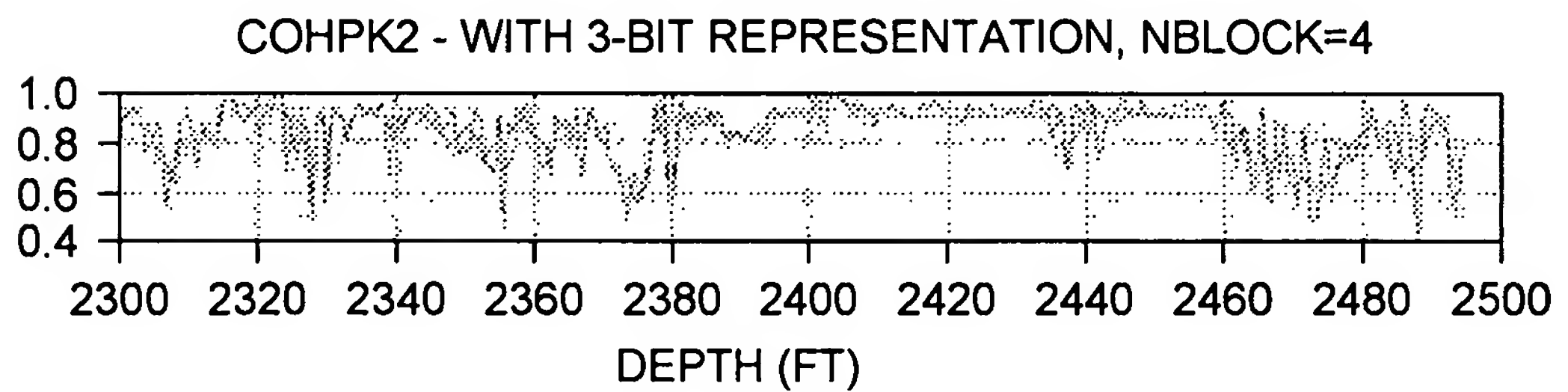
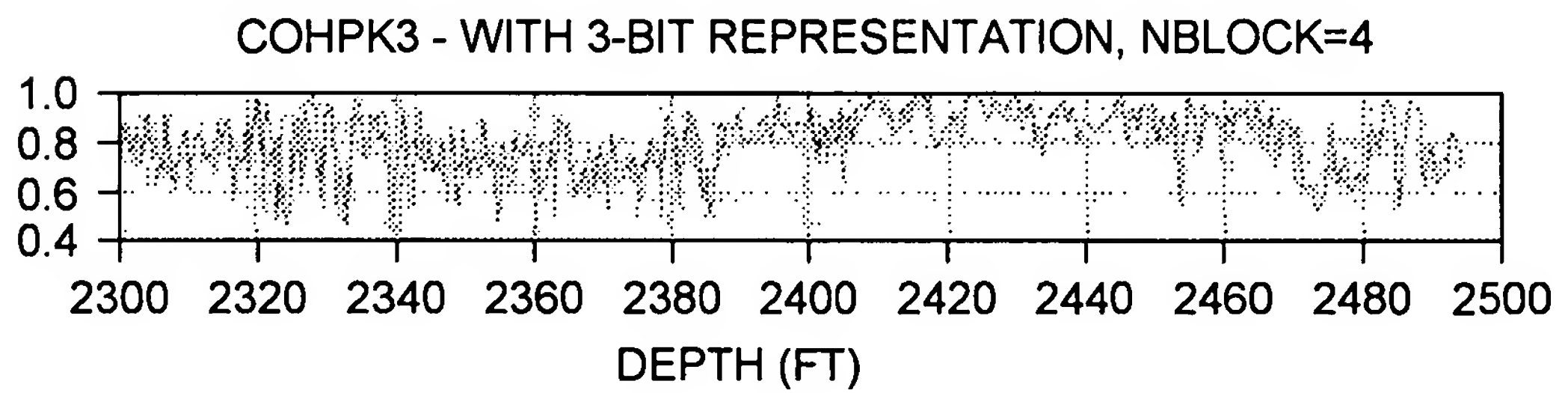
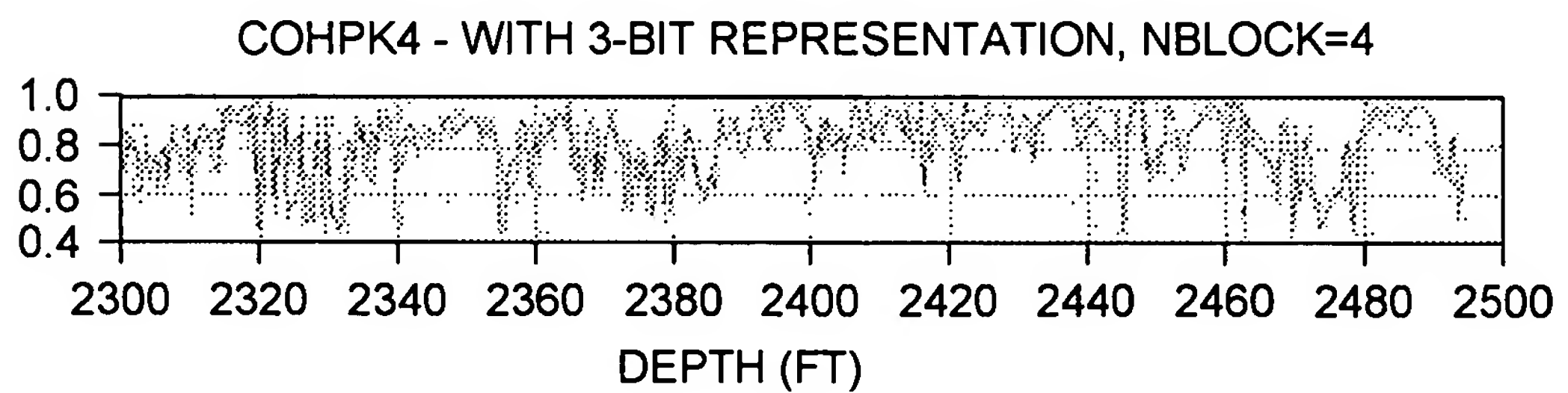


Fig. 6C

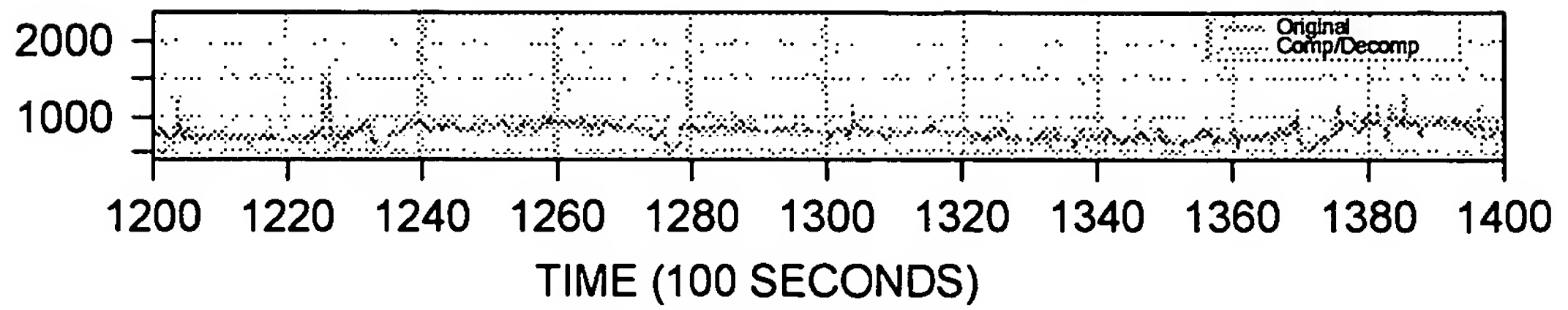
*Fig. 7A**Fig. 7B**Fig. 7C**Fig. 7D*

*Fig. 8A**Fig. 8B**Fig. 8C**Fig. 8D*

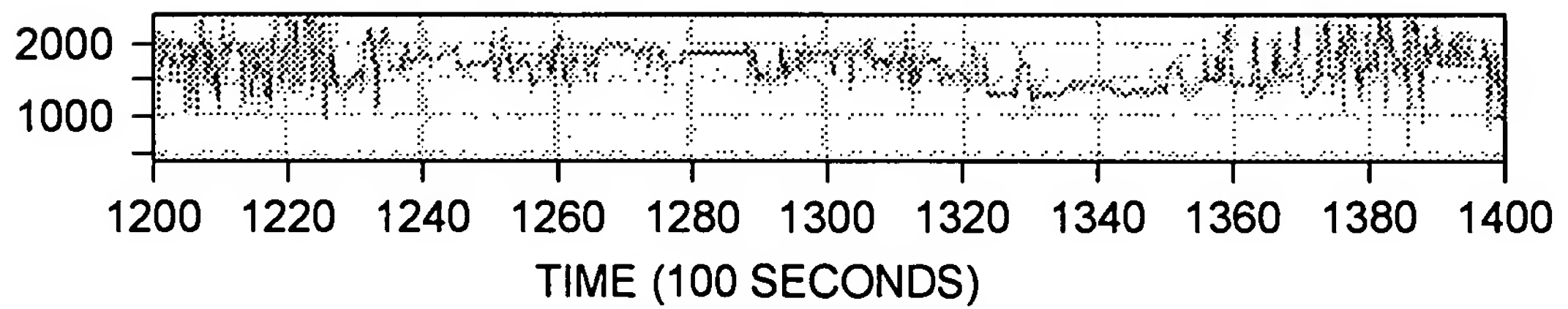
*Fig. 9A**Fig. 9B**Fig. 9C**Fig. 9D*

*Fig. 10A**Fig. 10B**Fig. 10C**Fig. 10D*

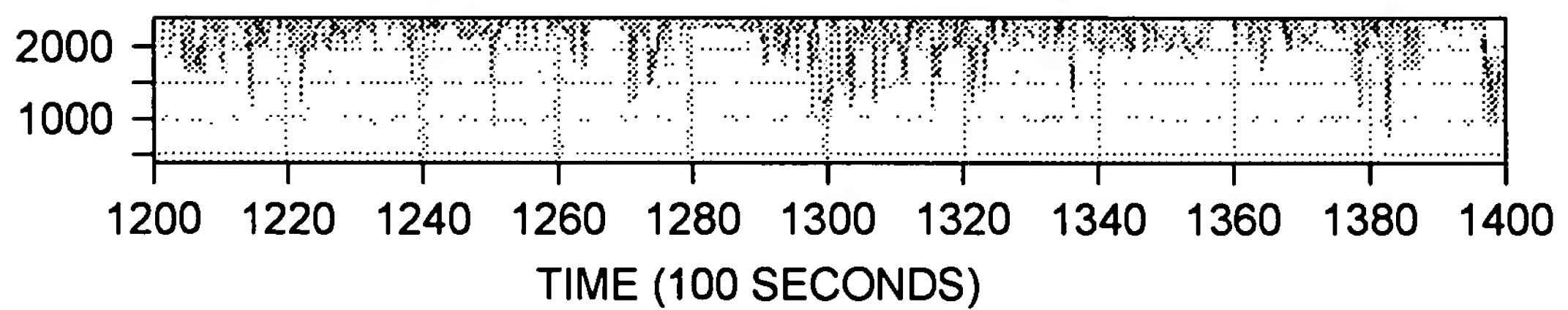
TTPK1 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 11A*

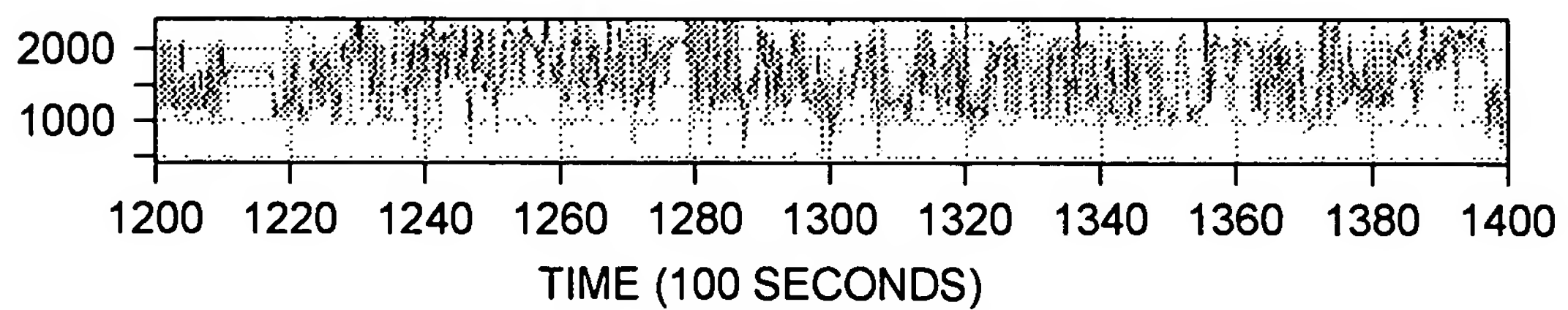
TTPK2 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 11B*

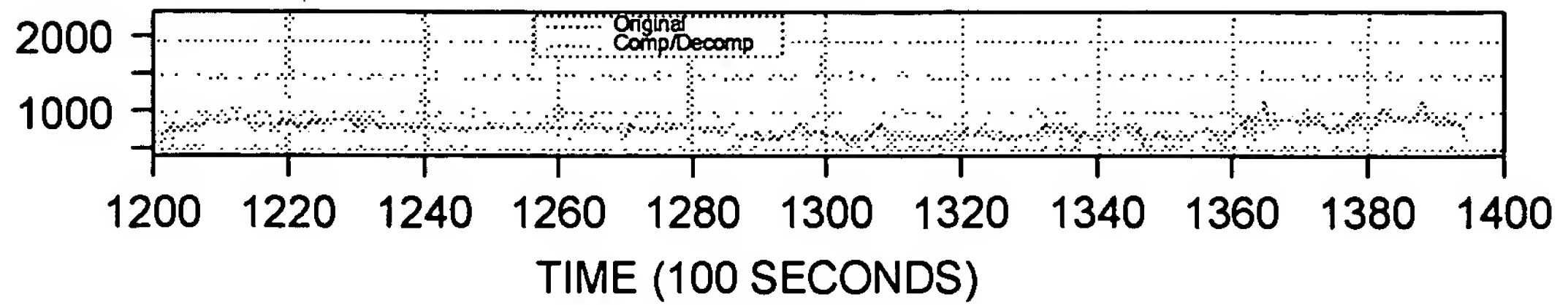
TTPK3 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 11C*

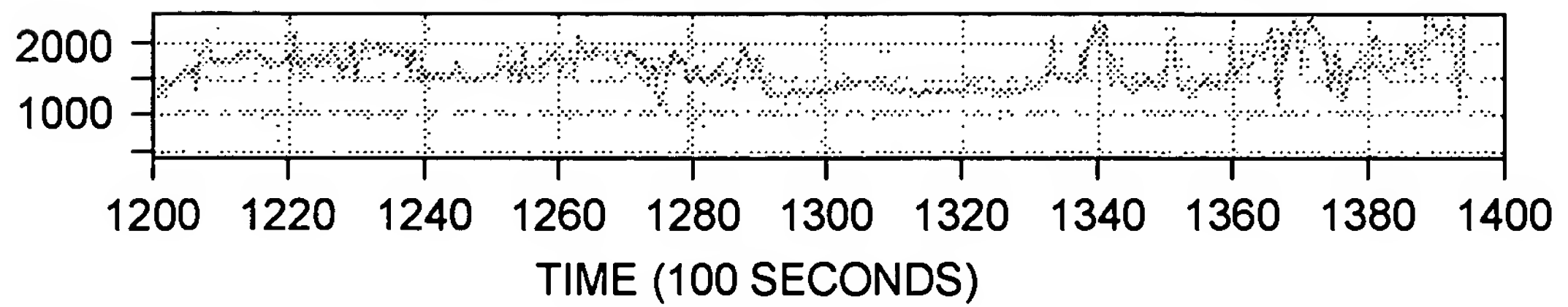
TTPK4 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 11D*

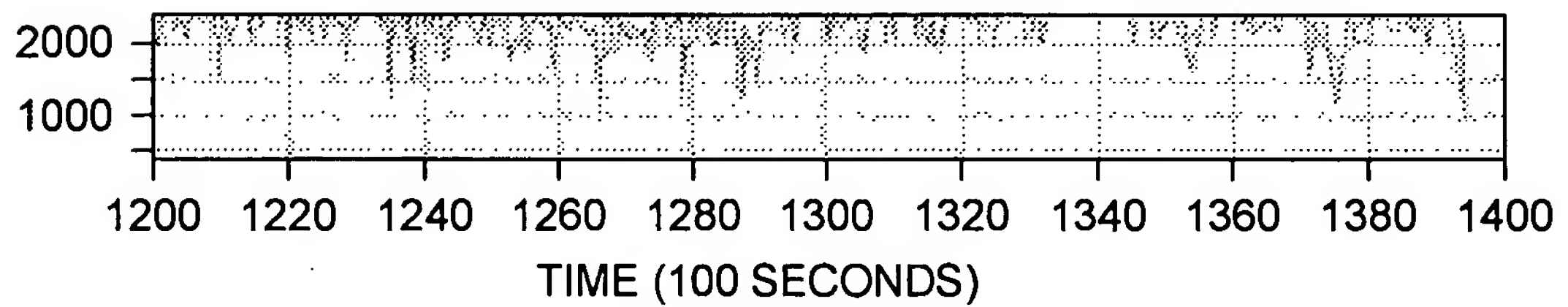
TTPK1 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 12A*

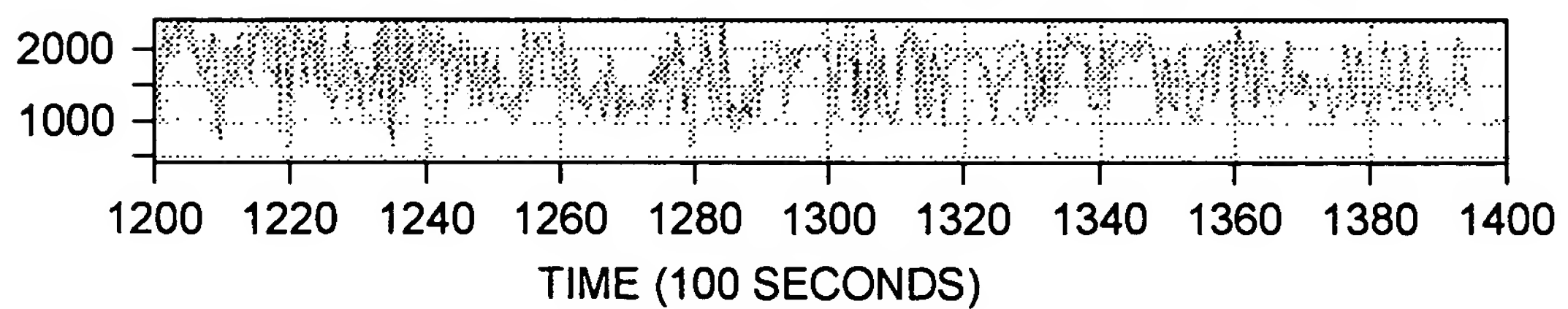
TTPK2 - WITH 4-BIT REPRESENTATION, NBLOCK=4

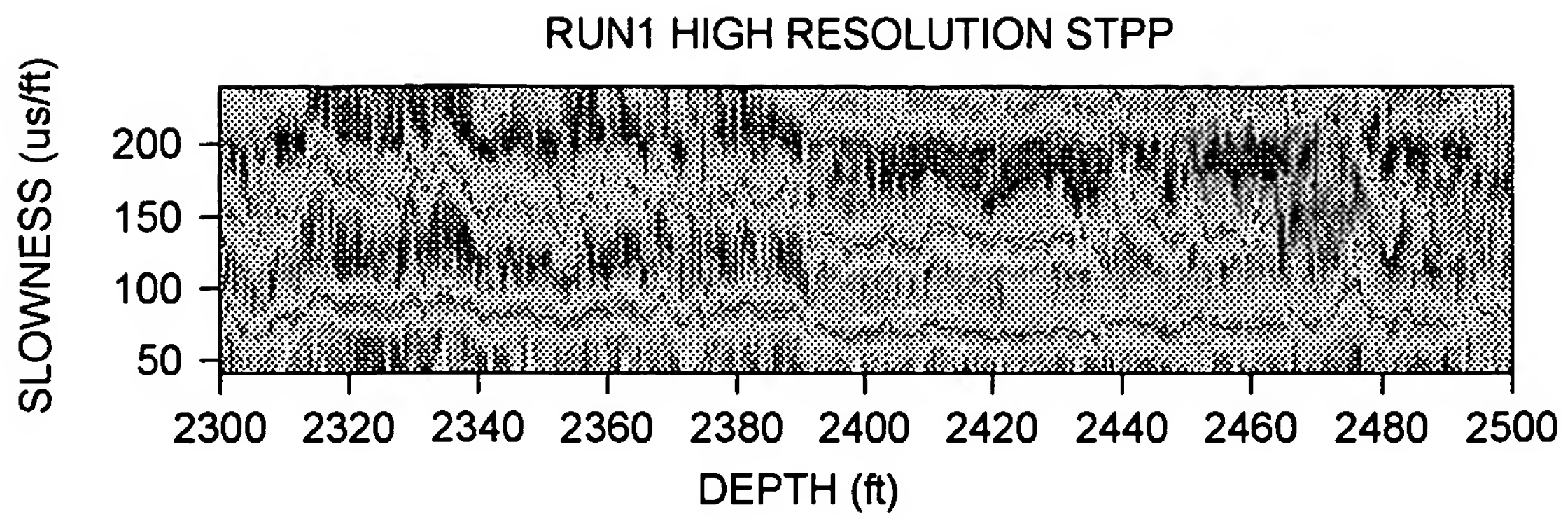
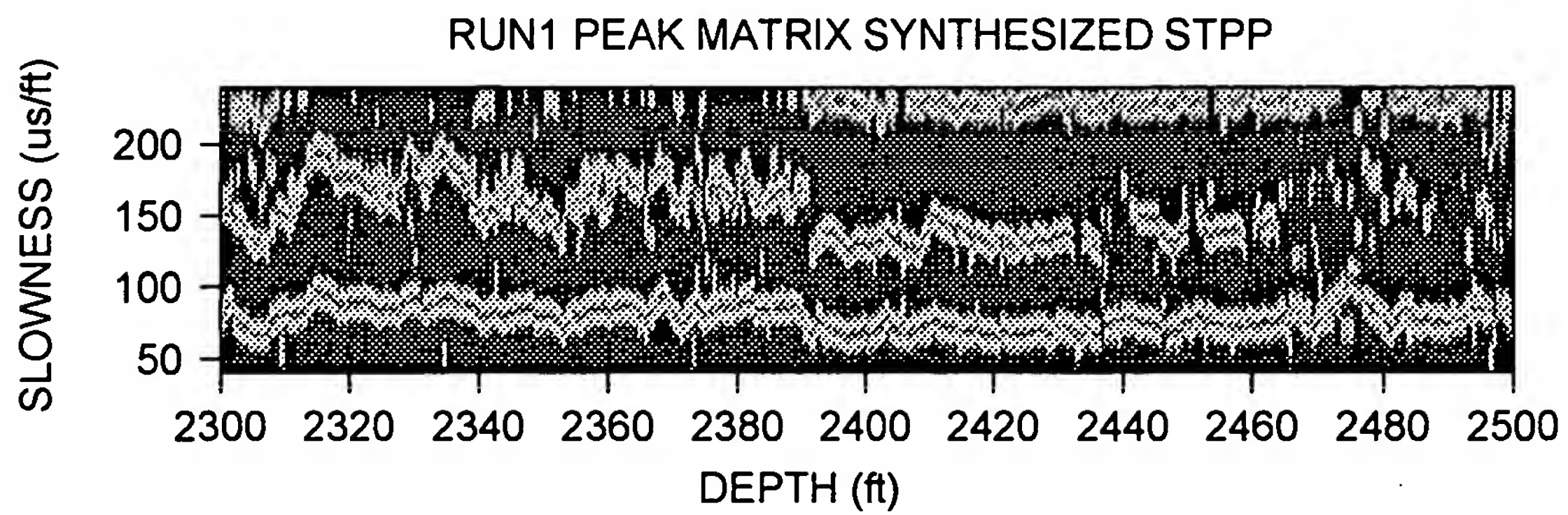
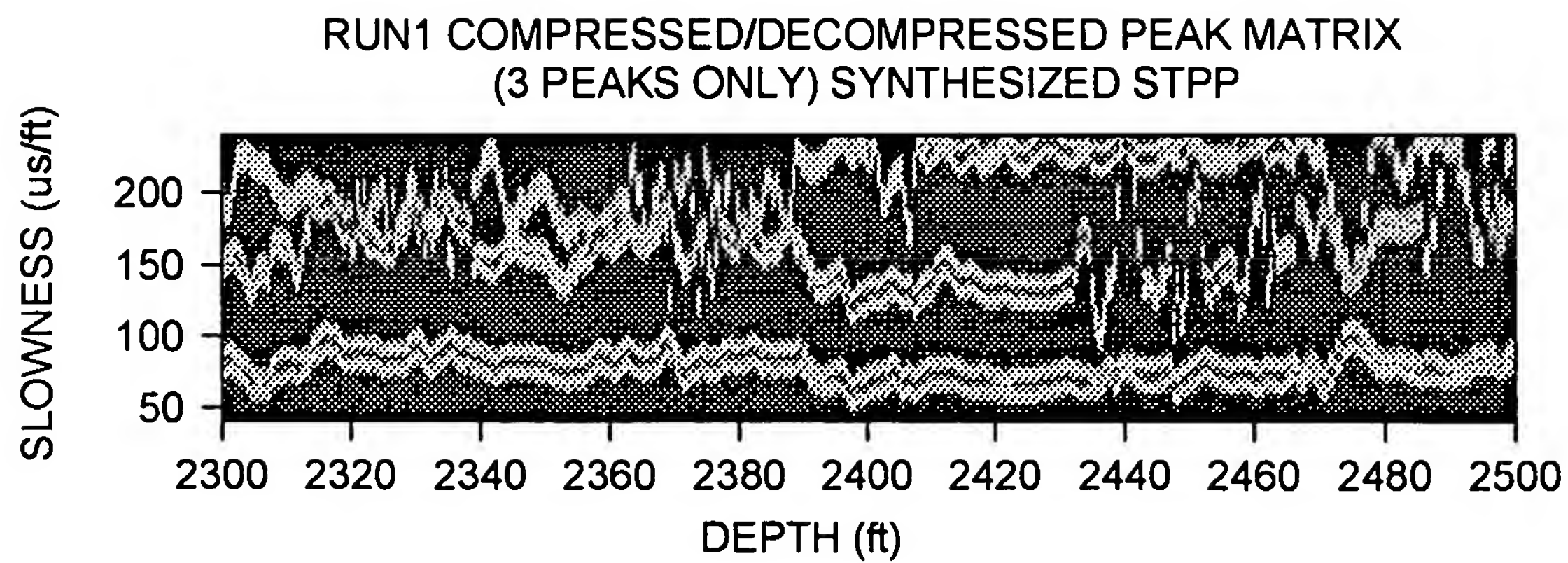
*Fig. 12B*

TTPK3 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 12C*

TTPK4 - WITH 4-BIT REPRESENTATION, NBLOCK=4

*Fig. 12D*

*Fig. 13A**Fig. 13B**Fig. 13C*

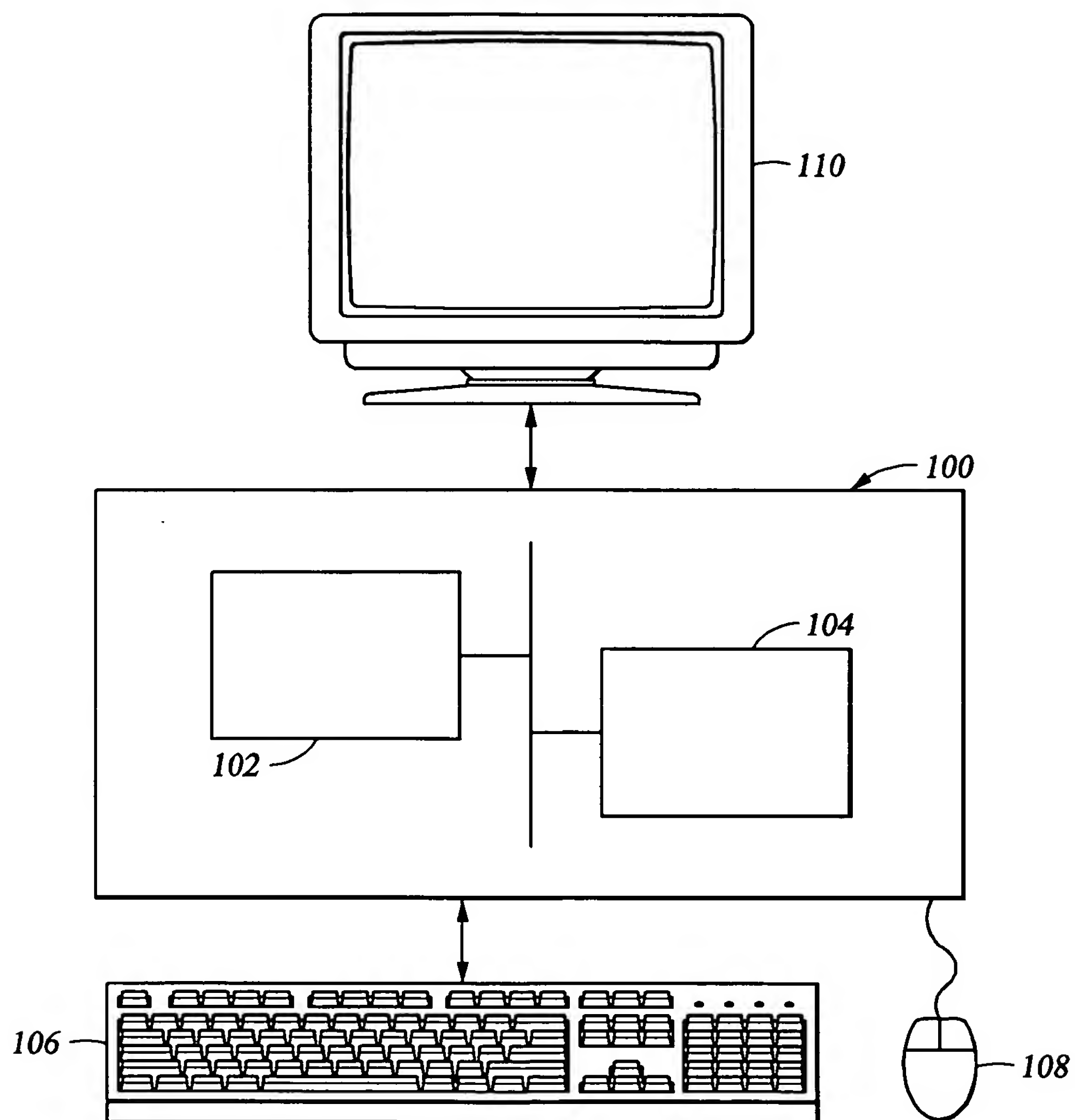


Fig. 14
(PRIOR ART)